IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

APPLICANTS: STEWART, KENNETH A. ET AL.   CASE NO.:   CS11493

SERIAL NO.:   09/986,241   GROUP: 2661

FILED: 10/22/01   EXAMINER: CHAUDRY, MUJTABA

ENTITLED: BLOCK PUNCTURING FOR TURBO CODE BASED
INCREMENTAL REDUNDANCY

Motorola, Inc.
Law Department
600 North US Highway 45 AS437
Libertyville, IL 60048

REQUEST FOR RECONSIDERATION

Commissioner for Patents
Alexandria, VA 22313-1450

Sir:

Responsive to the Office Action mailed April 29, 2005, transmitted herewith please find a Fee Transmittal Form, Petition for Extension of Time and an Amendment Transmittal Form. Please amend the above-identified application as follows.

REMARKS

In the Office Action mailed April 29, 2005, the Examiner rejected claims 1-4, 8-10, and 14-17 under 35 USC Section 103(a) as being unpatentable over Yamaguchi et al. in view of Tong et. al.

Claim 1 recites:

A method of block puncturing for turbo code based incremental redundancy, the method comprising the steps of:

coding an input data stream into systematic bits and parity bits;

loading the systematic bits and parity bits into respective systematic and parity block interleavers in a column-wise manner;

selecting a predefined redundancy; and

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